

L Number	Hits	Search Text	DB	Time stamp
-	1474	714/25.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:47
-	39	714/25.ccls. and (floating near point)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:01
-	96	714/9.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 10:53
-	3	714/25.ccls. and (voltage and cpu).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:01
-	71	714/25.ccls. and (voltage).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:16
-	1242	324/73.1.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 10:41
-	48	324/73.1.ccls. and (floating)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:24
-	117	324/73.1.ccls. and cpu	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:02
-	555	324/509.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:34
-	48	324/509.ccls. and (cpu processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:36
-	70	324/500.ccls. and (cpu processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:42
-	122	324/522.ccls. and (cpu processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:46

-	32	324/523.ccls. and (cpu processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:47
-	12	324/524.ccls. and (cpu processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 11:48
-	152	324/537.ccls. and (cpu processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 12:00
-	6148	324/\$.ccls. and (cpu processor) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:03
-	2914	324/\$.ccls. and ((cpu processor) same voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:04
-	646	324/\$.ccls. and ((cpu processor) same voltage).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:05
-	54	324/\$.ccls. and ((cpu processor) same voltage same (error fault failure)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:08
-	28	324/\$.ccls. and (floating same (error fault failure)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:09
-	29	714/\$.ccls. and (floating same (error fault failure)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:15
-	30	714/\$.ccls. and ((cpu processor) same voltage same (error fault failure)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/01 13:44
-	344	324/\$.ccls. and ((cpu processor) same voltage same (measure\$4 determin\$4)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:31
-	6	324/\$.ccls. and ((cpu processor) near voltage same (measure\$4 determin\$4)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:32

-	0	324/\$.ccls. and ((cpu processor) near3 voltage same bios).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:42
-	0	324/\$.ccls. and ((cpu processor) same voltage same bios).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:42
-	3	714/\$.ccls. and ((cpu processor) same voltage same bios).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:43
-	299	714/\$.ccls. and ((cpu processor) same voltage same (measur\$5 determin\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:44
-	10	714/\$.ccls. and ((cpu processor) near3 voltage near3 (measur\$5 determin\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:46
-	160	324/\$.ccls. and ((cpu processor) near3 voltage near3 (measur\$5 determin\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:47
-	51	324/\$.ccls. and (cpu near3 voltage near3 (measur\$5 determin\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:52
-	78	713/300.ccls. and (cpu same voltage same detect\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:07
-	54	714/\$.ccls. and (cpu and voltage).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:28
-	16	714/30.ccls. and (cpu same voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:32
-	5	714/37.ccls. and (cpu same voltage)	USPAT; US-PGPUB;	2004/10/04 13:34
-	483	714/\$.ccls. and (cpu same voltage)	EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:34

-	112	714/\$.ccls. and (cpu same voltage same (error fault failure))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:37
-	4165	324/\$.ccls. and (voltage same (determin\$4 measur\$5) same (error fault failure))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:38
-	7	324/\$.ccls. and (floating same ground same voltage same (determin\$4 measur\$5) same (error fault failure))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:39
-	497	714/10.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:44
-	23	714/10.ccls. and (voltage and floating)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 13:49
-	1273	327/\$.ccls. and (voltage and cpu)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:55
-	34	327/\$.ccls. and (voltage same cpu).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:01
-	18468	327/\$.ccls. and (voltage).ab.	USPAT; US-PGPUB; EPO; IBM_TDB	2004/10/05 11:24
-	1599	327/\$.ccls. and (voltage).ab. and (cpu processor)	USPAT; US-PGPUB; EPO; IBM_TDB	2004/10/05 11:25
-	960	327/\$.ccls. and (voltage).ab. and (cpu processor) and detect\$4	USPAT; US-PGPUB; EPO; IBM_TDB	2004/10/05 11:25
-	298	327/\$.ccls. and (voltage and detect\$4).ab. and (cpu processor)	USPAT; US-PGPUB; EPO; IBM_TDB	2004/10/05 11:28
-	21	327/\$.ccls. and (voltage and detect\$4 and (cpu processor)).ab.	USPAT; US-PGPUB; EPO; IBM_TDB	2004/10/05 11:28
-	122	714/709.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:33

-	4	714/709.ccls. and (cpu processor).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:34
-	241	714/721.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:35
-	5	714/721.ccls. and (cpu processor).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:35
-	16	714/721.ccls. and cpu and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:40
-	8	714/9.ccls. and cpu and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:41
-	10	714/9.ccls. and (cpu processor) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:42
-	3296	714/\$.ccls. and (cpu processor) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:42
-	196	714/\$.ccls. and (cpu processor) and voltage and floating and ground	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:42
-	19	714/\$.ccls. and ((cpu processor) same voltage) and (floating same ground)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:46
-	30	714/30.ccls. and ((cpu processor) same voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:50
-	14	714/37.ccls. and ((cpu processor) same voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 11:52
-	57	327/\$.ccls. and (cpu same (error fault) same signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:07

-	486	714/\$.ccls. and (cpu same (error fault) near3 signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:07
-	318	714/\$.ccls. and (cpu same (error fault) near signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:08
-	20	714/\$.ccls. and (cpu same (error fault) near signal\$1 same voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:12
-	43	714/\$.ccls. and (floating adj point adj (error\$1 fault\$1 exception\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:20
-	46	324/\$.ccls. and ((cpu processor) near3 (error\$1 fault\$1 exception\$1)) same voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:27
-	1046	712/\$.ccls. and ((error\$1 fault\$1 exception\$1) near3 signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:27
-	598	712/\$.ccls. and ((error\$1 fault\$1 exception\$1) near3 signal\$1 and floating)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:28
-	3	712/\$.ccls. and ((error\$1 fault\$1 exception\$1) near3 signal\$1 and (floating same ground))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:29
-	230	712/\$.ccls. and (floating adj point adj (error\$1 fault\$1 exception\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:39
-	6	712/\$.ccls. and (floating adj point adj (error\$1 fault\$1 exception\$1)) and voltage	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:44
-	204	712/\$.ccls. and (floating adj point adj (error\$1 fault\$1 exception\$1)) and (voltage signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/06 15:42
			EPO; JPO; DERWENT; IBM_TDB	